

In re Application of:
Regula, Jack
Serial No. 08/771,581
Filed december 20, 1996



Attached are Information Disclosure Statement Reference Sheets ⁴~~(2)~~ from Parent Application
S/N 08/771,581, Filed December 20, 1996.

If needed, actual references will be provided if requested.

A handwritten signature, possibly reading "JBC", located in the center-right area of the page.

Art Cited

United States Patent Numbers:

5,764,924

Foreign Patent Documents:

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

SERIAL NO.

GROUP ART UNIT

FILING DATE

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(37 CFR 1.98(b))

APPLICANT(S)

Jack Regula

ATTY. DOCKET NO.

SS-696-01

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
	L						
	M						
	N						
	O						
	P						
	Q						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	R	Master Thesis by IVAN TVING, August 28, 1994 "Multiprocessor interconnection using SCI"
	S	PCI Local Bus Specification, Revision 2.1, June '1, 1995 PCI Special Interest Group
	T	PCI to PCI Bridge Architecture Specification, Version 1.0, 1994 PCI Special Interest Group

EXAMINER

DATE

EXAMINER: Initial citation considered. Draw line through citation if not
in conformance and not considered. Include copy of this form with next
communication to applicant.